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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10043027	FILING DATE 01/09/2002	CLASS 438	SUBCLASS 396	GAU 2012	EXAMINER VESPERMAN
**APPLICANTS: Chung Jeong-hee; Park In-sung; Yoo Jae-hyun; 2813					
**CONTINUING DATA VERIFIED: <i>we</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>we</i> REPUBLIC OF KOREA 2001-2990 01/18/2001					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>			RESCIND <input type="checkbox"/>		
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no			ATTORNEY DOCKET NO		
35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no			5649-917		
Written and Acknowledged Examiners's initials <i>we</i>					
TITLE: Methods of forming an integrated circuit capacitor in which a metal preprocessed layer is formed on an electrode thereof					
U.S. DEPT. OF COMMERCE TECHNOLOGICAL CENTER					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg. Print Fig.
ISSUE FEE		Application Examiner	
Amount Due	Date Paid		
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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